

REVISION HISTORY

Version	Date	Engineer	Change Description
R0A	2017/11/20	Dino_Lin	Initial release

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08	PWR_USB_VCC5P0	16	Connection Board Mounting

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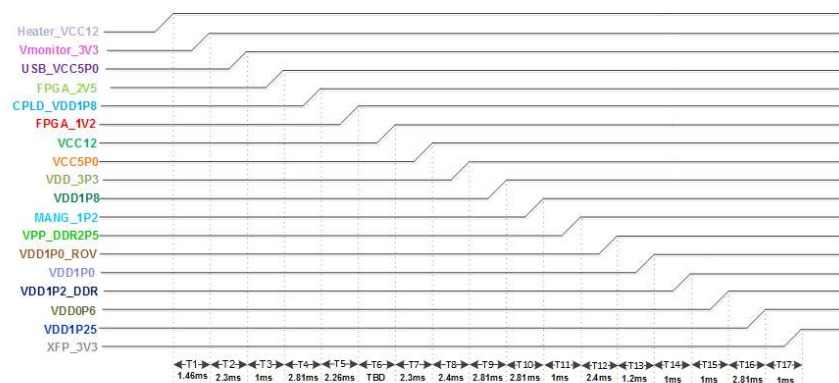
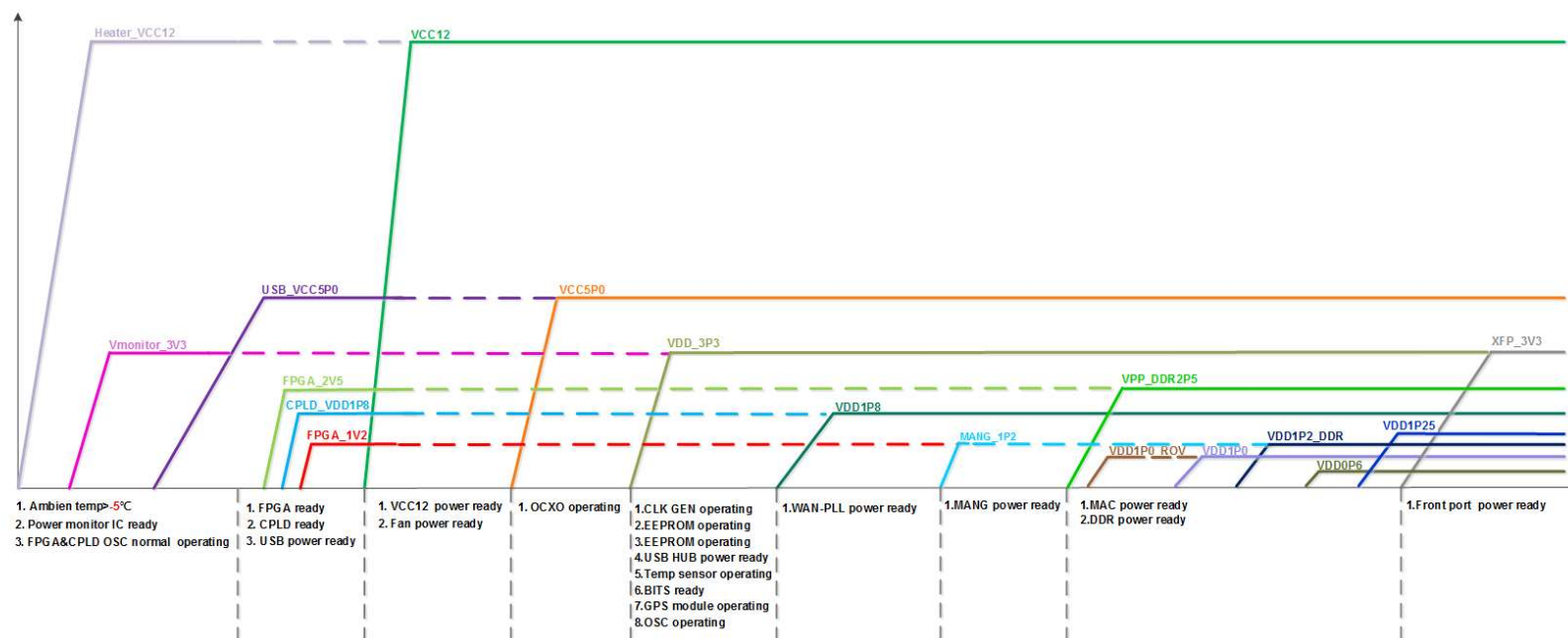
Model Name
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Rev
R0C

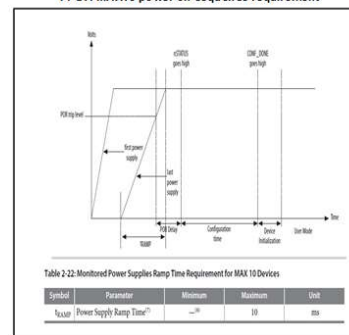
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Page Name
Index List

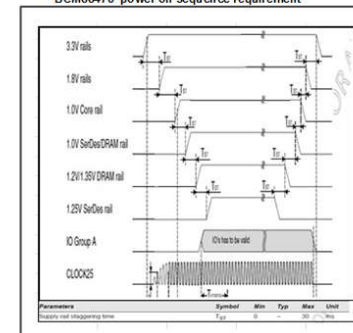
Engineer
Dino_Lin



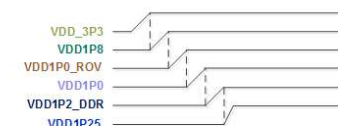
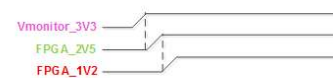
FPGA-MAX10 power on sequence requirement



BCM88470 power on sequence requirement



	System power on delay time(ms)																
	T1	T2	T3	T4	T5	T6	T7	T8	T9	T10	T11	T12	T13	T14	T15	T16	T17
DC/DC Soft-start	0.46	1.3	-	1.81	1.26	-	1.3	1.4	1.81	1.81	-	1.4	0.2	-	-	1.81	-
UCD96160 turn on delay	1	1	1	1	1	TBD	1	1	1	1	1	1	1	1	1	1	1
Total delay	1.46	2.3	1	2.81	2.26	TBD	2.3	2.4	2.81	2.81	1	2.4	1.2	1	1	2.81	1



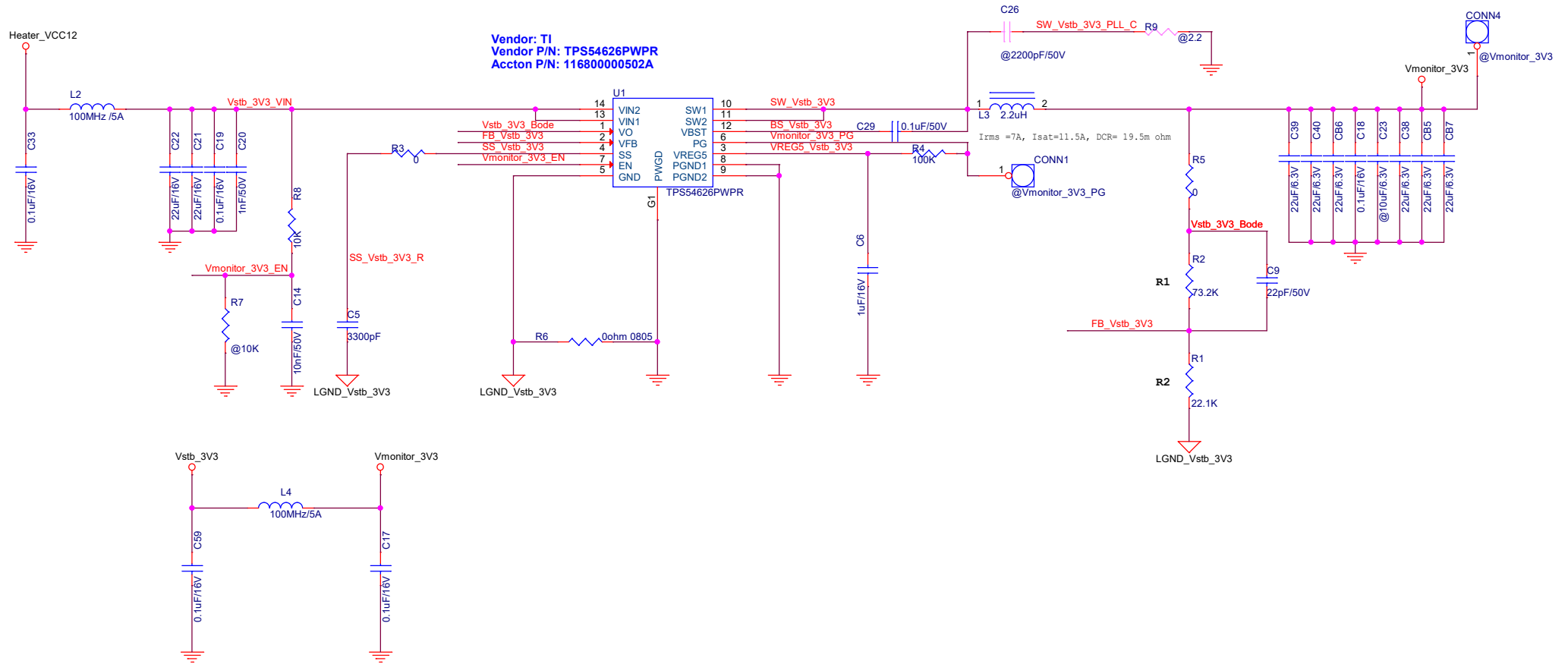
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Vmonitor_3V3/ 4.5A

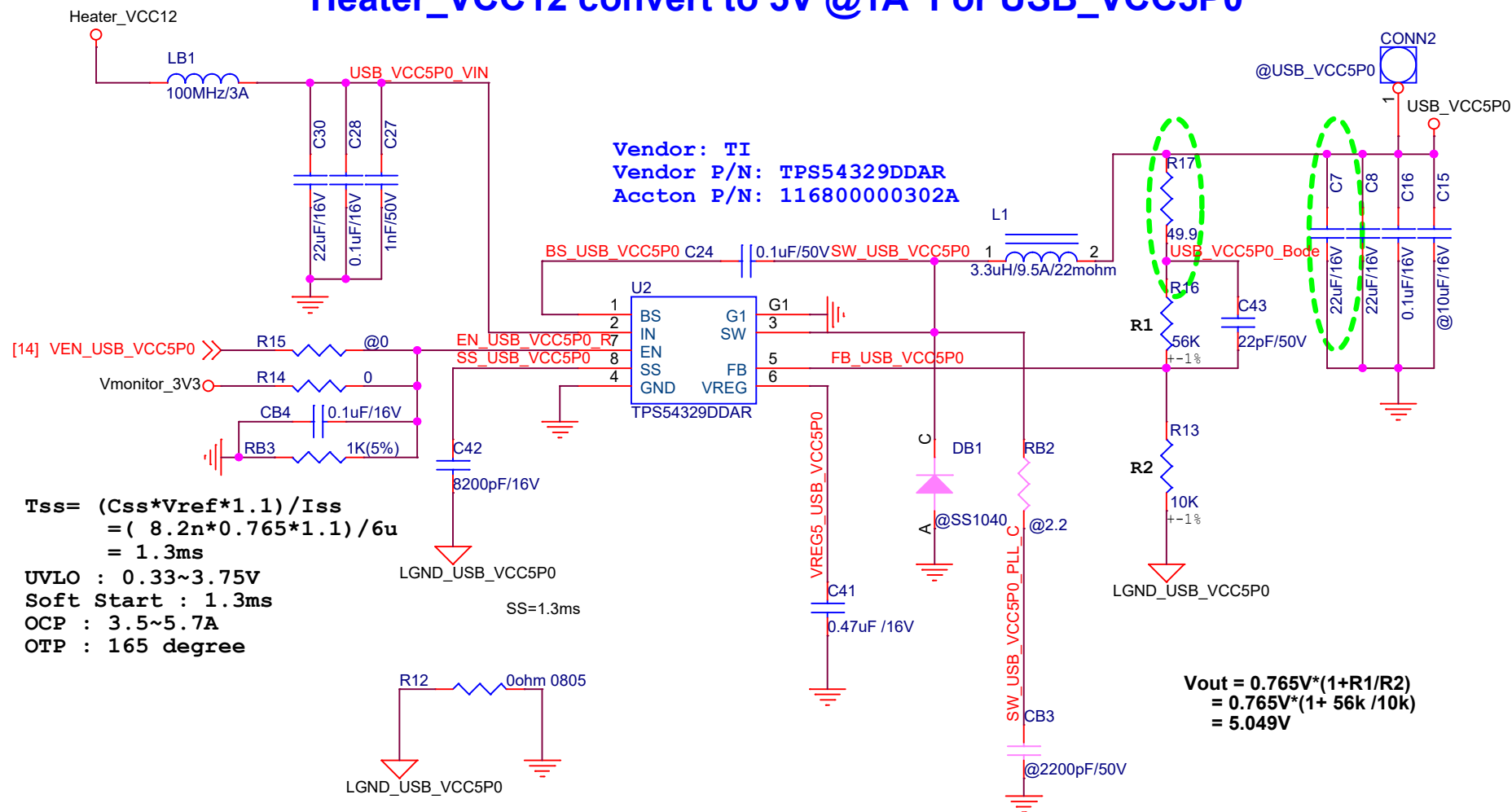


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Heater_VCC12 convert to 5V @1A For USB_VCC5P0



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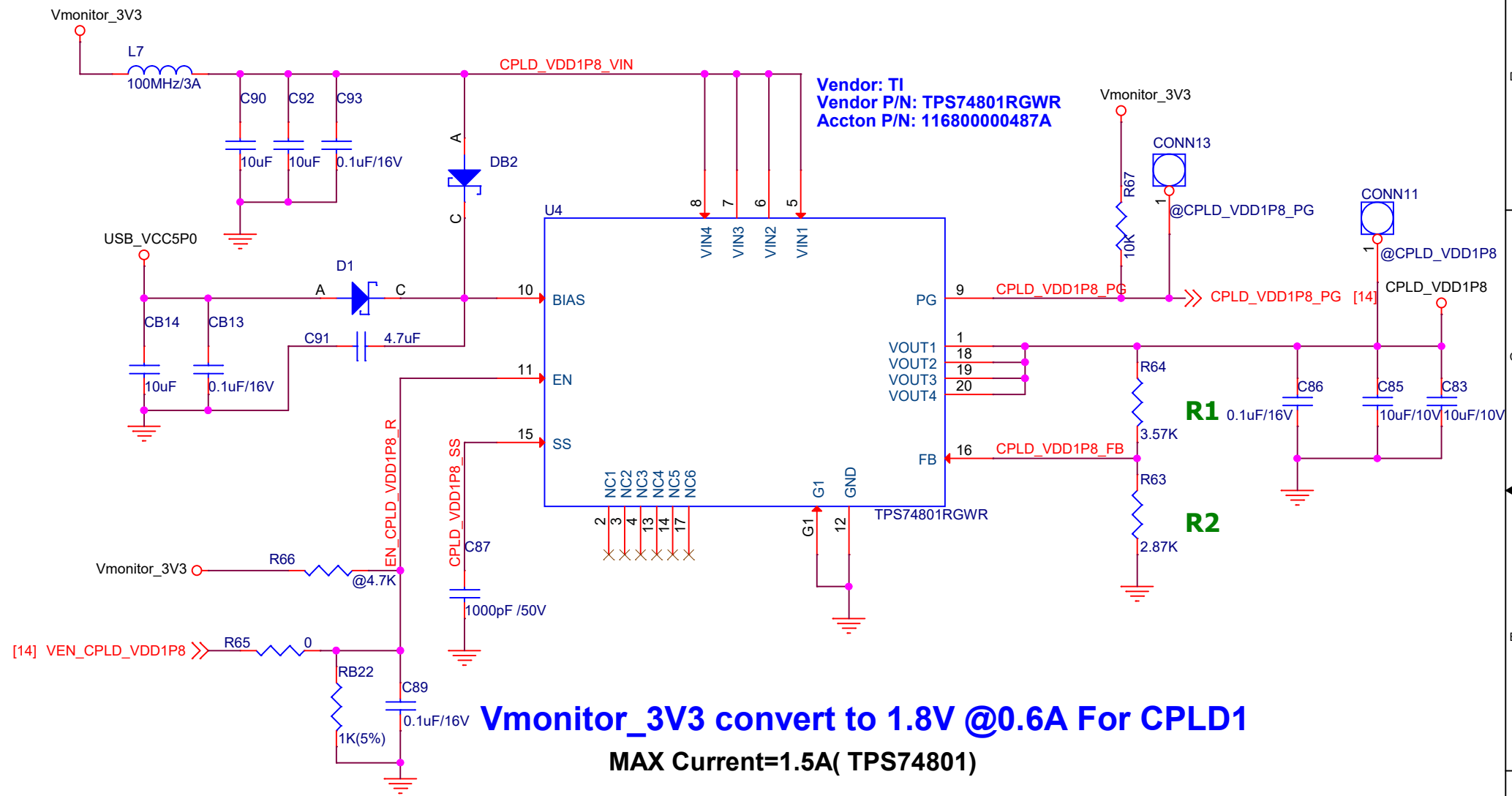
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Size A	Page Name PWR USB VCC5P0
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Engineer
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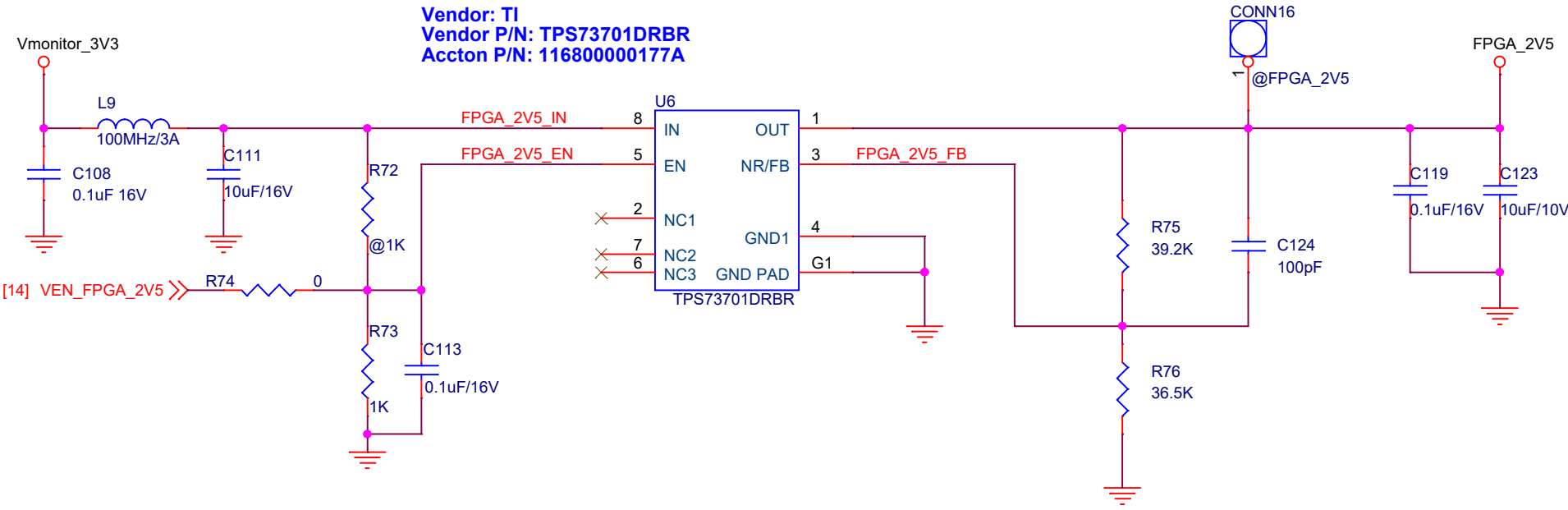


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FPGA_2V5/ 0.25A

Vendor: TI
Vendor P/N: TPS73701DRBR
Accton P/N: 116800000177A



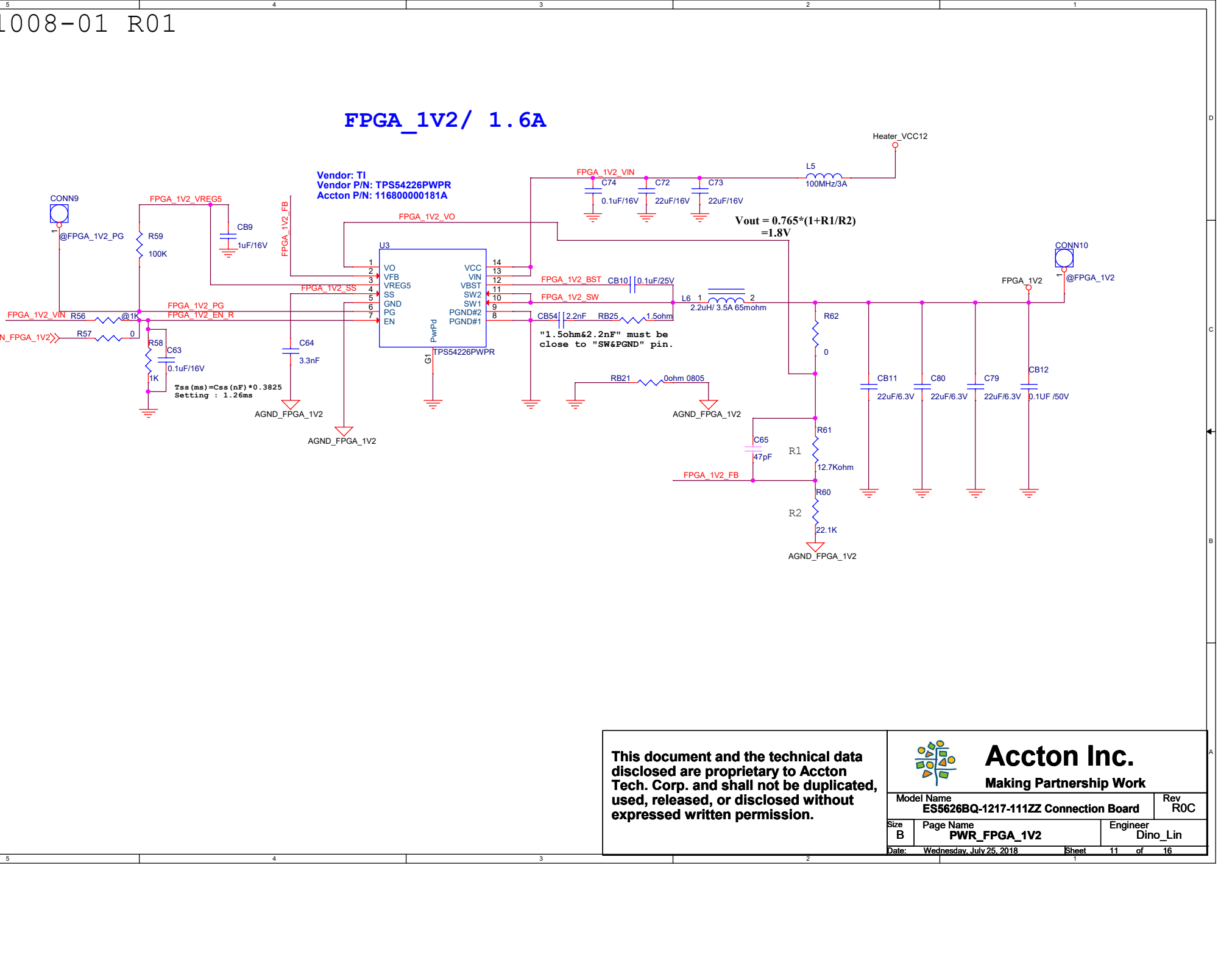
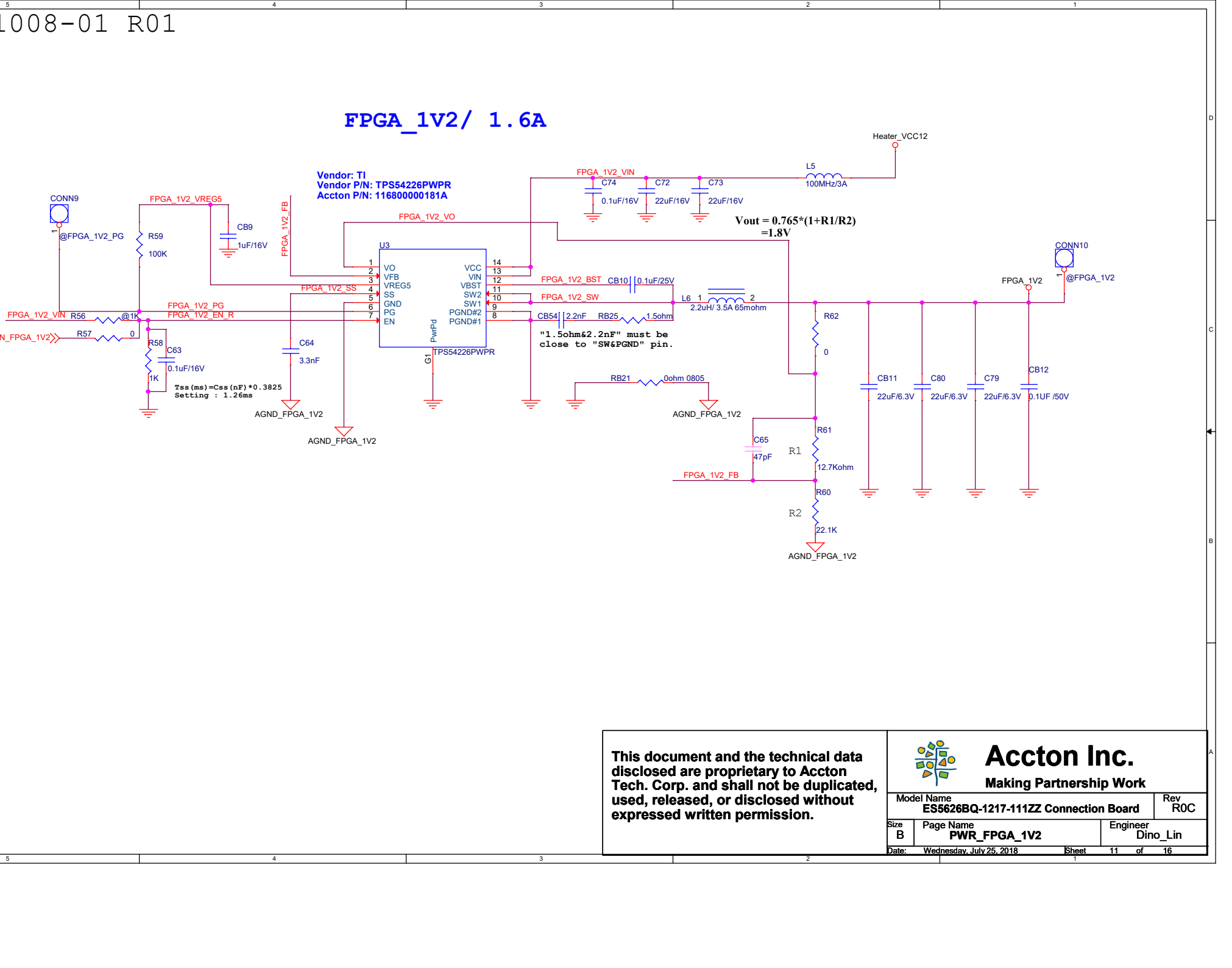
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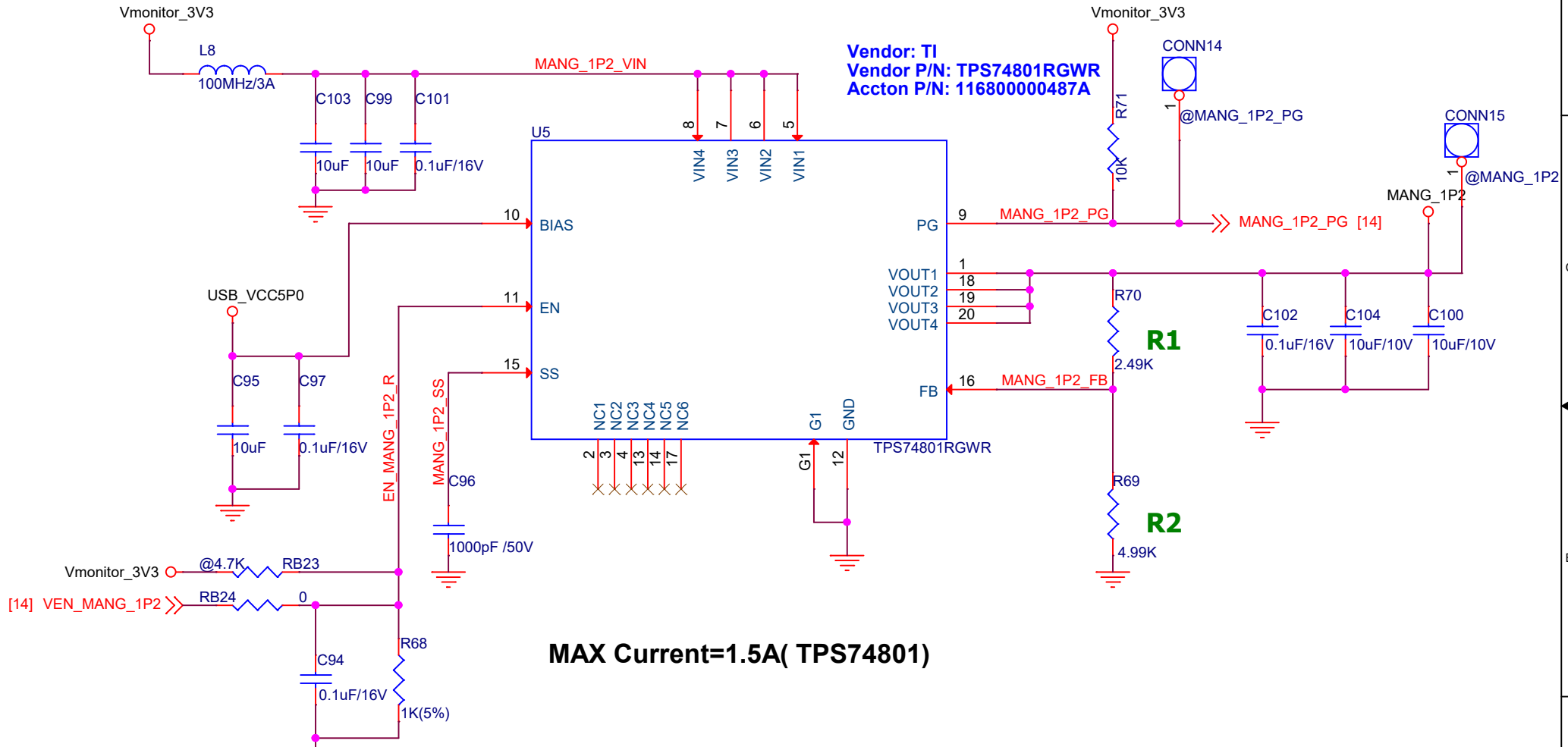
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[illegible][illegible][illegible]

MANG_1P2/ 0.5A



Vendor: TI
Vendor P/N: TPS74801RGWR
Accton P/N: 116800000487A

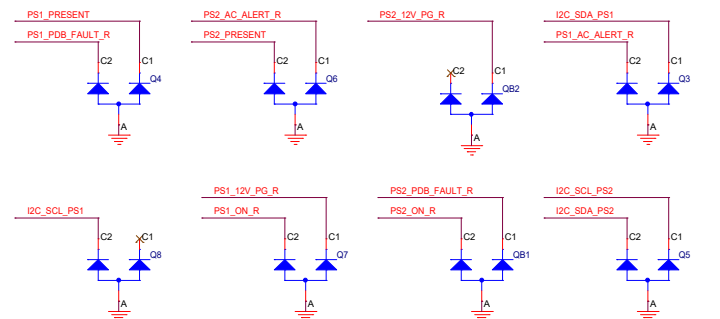
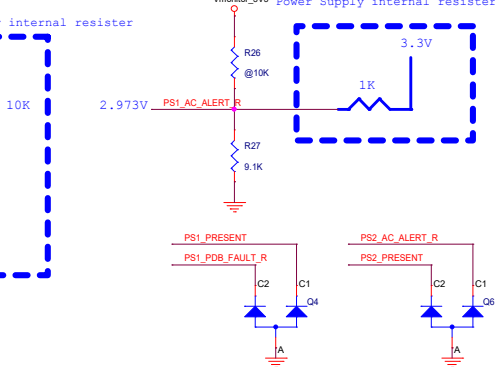
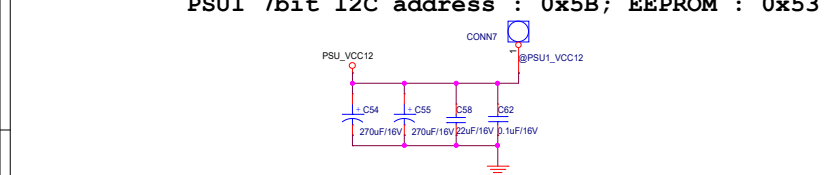
MAX Current=1.5A(TPS74801)

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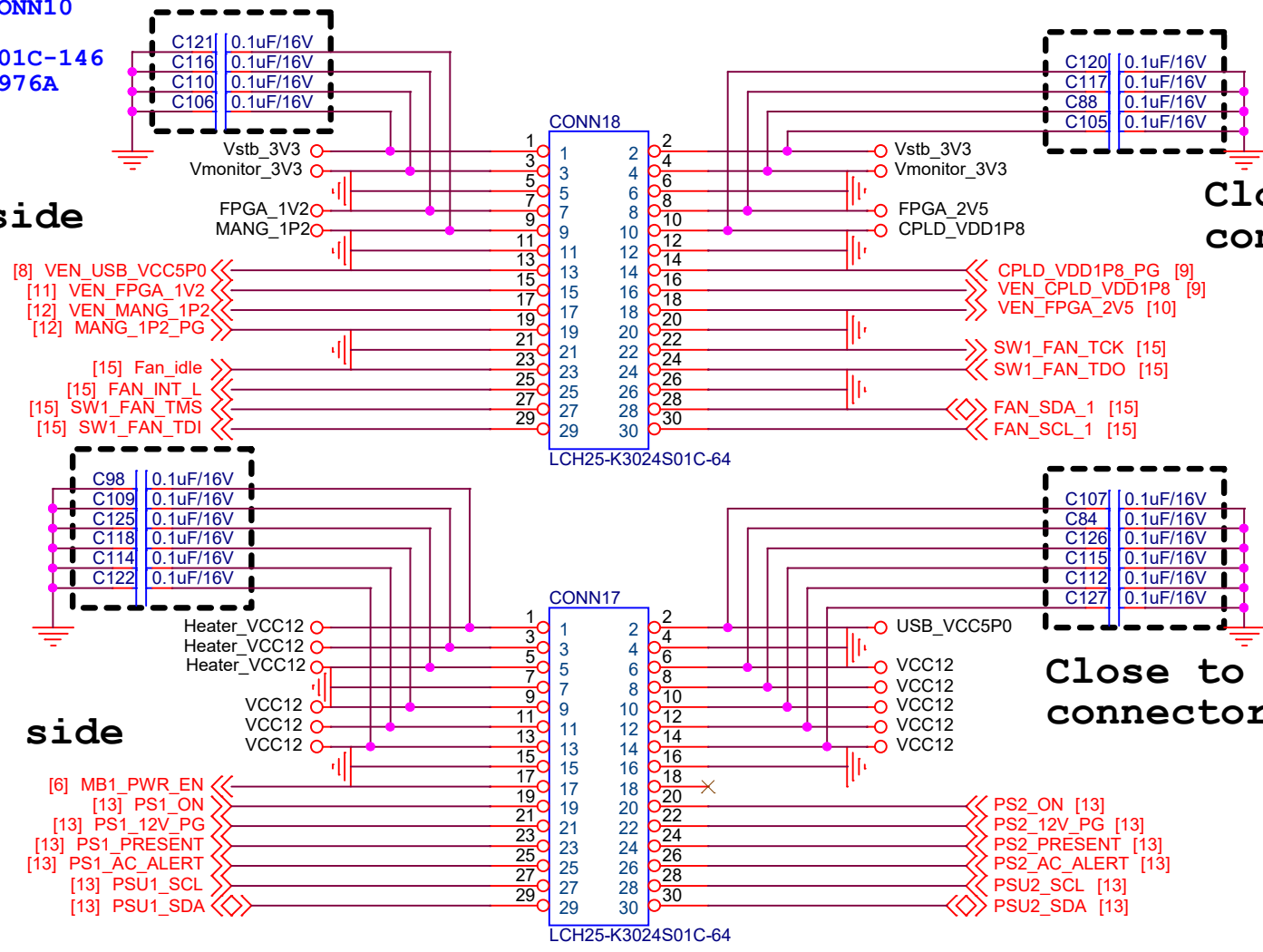
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FMA-HW01008-01 R01 Connection Board to Main Board CONN

Note: For CONN9&CONN10
Vendor: LCN
P/N: LCH25-K3024T01C-146
Accton: 118200000976A

Close to
connector side



Close to
connector side

Close to
connector side

Close to
connector side

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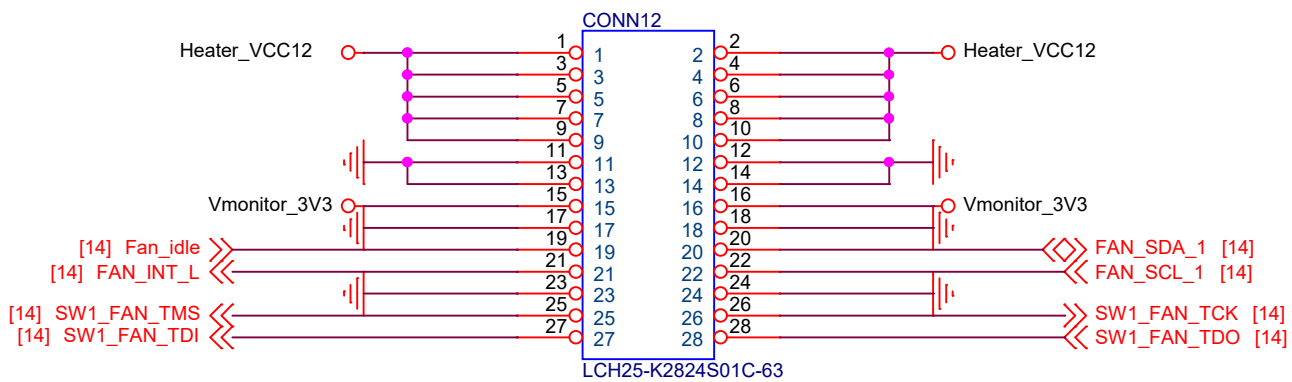


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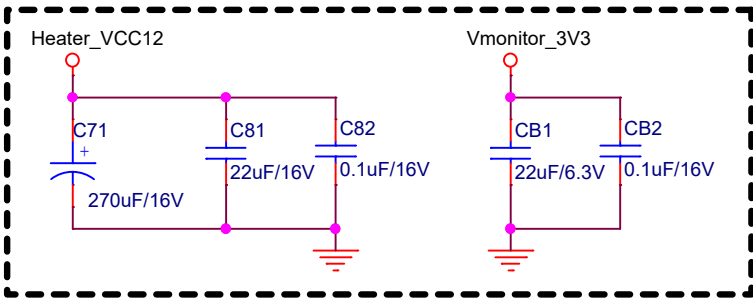
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Connection Board to Fan Board CONN

Vendor: LCN
P/N: LCH25-K2824S01C-63
Accton: 118200000978A



Close to connector side



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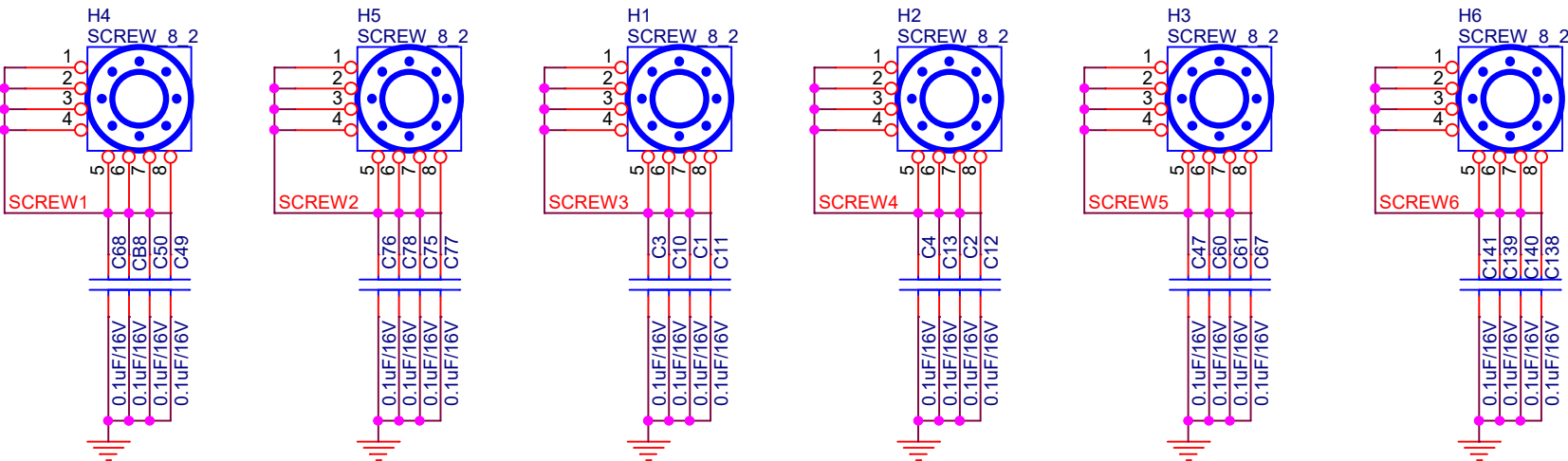


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Mounting Hole



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